Demonstrating New Foundations

MultiLane at ECOC

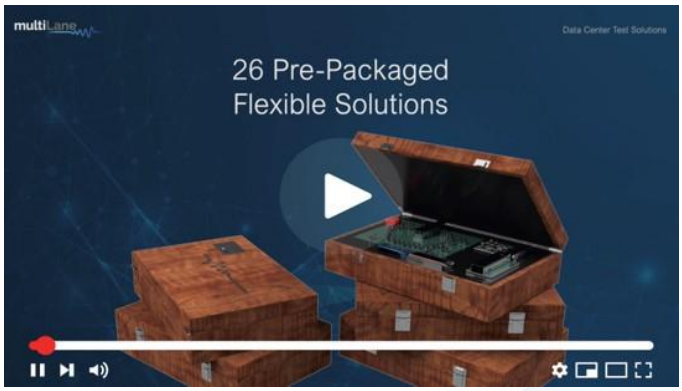
MultiLane returns to European Conference on Optical Communication (ECOC), Europe's biggest optical communications exhibition. Practical applications of our products can be found in booths all around the exhibition, so keep an eye out for our signature look. Make sure to pass by booth #276, where our team cannot wait to meet and discuss innovative solutions to tackle the upcoming 800G wave and beyond! Register to join the ECOC here and find out more about it here

Stress Input Tolerance (SITOL) A New Validation Cycle

As speeds increase to 800G and above, so does sensitivity to disruption. In anticipation of the need to account for these disruptions, MultiLane has devised a new holistic validation cycle, incorporating Jitter, Interference, Power Integrity, and margin stress testing under the heading: Stressed Input Tolerance Testing (SITOL). Check out the first video in our SITOL Series

DCTS Development Kits

Deploy your designs a lightning speed with MultiLane's Data Center Test Solutions (DCTS) Development Kits: all-in-one solutions to facilitate module, switch, and compliance testing. With 26 kits from 50-800G, MultiLane's DevKits offer a straightforward solution to modern data center design and deployment



Innovation for the next generation

For any questions, please contact us at sales@multilaneinc.com.

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